R16

Q.P. Code: 16CS524

Reg. No:

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR

(AUTONOMOUS)

B.Tech III Year II Semester Supplementary Examinations July-2021 COMPILER DESIGN

(Common to CSE & CSIT)

Time: 3 hours	Max. Marks: 60
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	V P *** ****	
	(Answer all Five Units $5 \times 12 = 60$ Marks)	
	UNIT-I	
1	a Describe the role of lexical analysis in compiler design.	6 M
	b Explain the Structure of Compiler.	6 M
	OR	
2	a Explain Input Buffering with simple examples.	6 M
	b List the various phases of a compiler and Define Regular Expressions and Regular	6M
	Grammar.	OIVI
	UNIT-II	
3	a Calculate FIRST and FOLLOW for the following grammar.	
	a) $E > E + T/T$, $T > T*F/F$, $F > (E)/id$	6M
	b Construct Predictive Parse Table for the grammar	
	E->E+T/T, T->T*F/F, F->(E) id and parse the string id+id*id.	6 M
	OR	
4	a Construct the recursive decent parser for the following grammar.	714
	$E \to E + T/T$, $T \to T * F/F$, $F \to (E)/id$	7M
	b Differences between SLR, CLR, and LALR parsers.	5M
	UNIT-III	
5	a Explain in detail about Back patching Technique.	6M
	b Write the Syntax of case statement with example.	6M
	OR	01.1
6	a Explain the Type Checking with suitable examples.	8M
U	b Define annotated parse tree with example.	4M
	UNIT-IV	•1112
7		ONA
7	a Explain Storage allocation strategies with suitable examples.	8M
	b Name any four procedural optimization techniques.	4M
0	OR	
8	a Explain about Global data flow analysis.	6M
	b Distinguish between static scope and dynamic scope.	6 M
	UNIT-V	
9	a Explain the target machine architecture.	8M
	b Give the different forms in target program.	4M
	OR	
10	a Describe the various strategies in register allocation.	4M
	b Explain the simple code generator and generate target code sequence for the	8M
	following statement $d:=(a-b)+(a-c)+(a-c)$.	OIVI

*** END ***